

UG PROGRAMME TIME TABLE YEAR: 2019 – 20 SEM: I, WITHEFFECT FROM: 1ST July, 2019


B. E. ELECTRICAL ENGG.


SR.NO.	TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
1	9.30AM TO 10.30AM	POH(SYG) [C-19]	B1- LIBRARY B2- ASP(DSP) [C-19] B3- CMP(STSB) [C-19]	B1- CMP(STSB) [C-19] B2- SEMI(VBS) [C-19] B3- LIBRARY [C-19]	FACTS(SPP) [C-19]	POH(SYG) [C-20]	B1- PP-1 B2- PP-1 B3- PP-1 B4- PP-1
2	10.30AM TO 11.30AM	FACTS(SPP) [C-19]	B4- SEMI(SAP) [C-19]	B4- ASP(DSP) [C-19]	POH(SYG) [C-19]	EEI(SAP) [C-20]	B1- PP-1 B2- PP-1 B3- PP-1 B4- PP-1
11.30 AM TO 12.10PM LUNCH BREAK							
3	12.10PM TO 1.10PM	B1- ASP(DSP) [C-20] B2- CMP(STSB) [C-20] B3- SEMI(VBS) [C-20] B4- LIBRARY [C-20]	CMPS(TSB) [C-19]	CMPS(TSB) [C-19]	ASP(DSP) [C-20]	B1- SEMI(SPP) [C-19] B2- LIBRARY [C-19] B3- ASP(DSP) [C-19] B4- CMP(STSB) [C-19]	B1- PP-1 B2- PP-1 B3- PP-1 B4- PP-1
4	1.10PM TO 2.10PM		POH(SYG) [C-19]	ASP(DSP) [C-19]	CMPS(TSB) [C-20]		
2.10PM TO 2.20 PM TEA BREAK							
5	2.20PM TO 3.20PM	ASP(DSP) [C-20]	ASP(DSP) [C-20]	FACTS(SPP) [C-20]	IITB SPOKEN TUTORIALS	CMPS(TSB) [C-19]	GATE MOCK TEST/ OPEN BOOK TEST
6	3.20PM TO 4.20PM	SOFTSKILL [C-20]	EEI(SAP) [C-20]	IIT(T)SAP [C-20]		FACTS(SPP) [C-19]	
7	4.30PM TO 5.30PM						

* indicates R/M/T/C/E (Remedial/Makeup/ Test/Counseling/Extra)


- 1) EE:-Economics for Engineers
- 3) CMPS:-Computer Methods for Power System
- 5) FACTS :-Flexible AC Transmission Systems
- 7) SEMI- Seminar

- 2) ASP :-Advanced Switchgear & Protection
- 4) POH:-Power Quality & Harmonics
- 6) PP-I :- Project Phase I


H. O. D.


Dean Academics




Principal

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P.V.P. INSTITUTE OF TECHNOLOGY, BUDHGAON
DEPARTMENT OF ELECTRICAL ENGINEERING
UG PROGRAMME TIME TABLE YEAR: 2019 – 20 SEMI: I, WITHEFFECT FROM: 1st July, 2019

T. Y. ELECTRICAL ENGG. [CLASS ROOM NO. 19]

SR.NO.	TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
1	9.30AM TO 10.30AM	T1-PS-II[VBSI], T2-EM-II[NSP], T3-EM-II[MAC], T4- MPMC[SAP]	Elective-IV[NSP]	HRLP[MAC]	T1-EM-II[NSP], T2- MPMC[SAP], T3- PS-II[DSP], T4- EM-II[MAC]	ELECTIVE-V[VBSI]	OPEN BOOK TEST /TEST
	10.30AM TO 11.30AM		EM-II[LSP]	MPMC[SAP]		PS-II[DSP]	Elective-IV[NSP]*/ MPMC[SAP] / EM-II[LSP]
11.30 AM TO 12.10PM LUNCH BREAK							
3	12.10PM TO 1.10PM	EM-II[LSP]	T1- MPMC[SAP], T2-EM-II [NSP], T3-EM-II[MAC], T4- PS-II[VBSI]	T1-EM-II[NSP] T2- PS-II[VBSI], T3- MPMC[SAP], T4- EM-II[MAC]	MPMC[SAP]	EM-II[LSP]	OPEN BOOK TEST /TEST ELECTIVE-V[VBSI]*/ PS-II[DSP]*/ HRLP[MAC]*
	1.10PM TO 2.10PM	Elective-IV[NSP]			HRLP[MAC]	SOFTSKILL	
2.10PM TO 2.20 PM TEA BREAK							
5	2.20PM TO 3.20PM	MPMC[SAP]	ELECTIVE-V[VBSI]	ELECTIVE-V[VBSI]	EM-II[LSP][T]		Elective-V[NSP]*/ MPMC[SAP] /EM-II[LSP]
	3.20PM TO 4.20PM	PS-II[DSP]	PS-II[DSP]	PS-II[DSP][T]	Elective-IV[NSP]	IITB SPOKEN TUTORIALS	ELECTIVE-V[VBSI]*/ PSII[DSP]*/HRLP[MAC]*
7	4.30PM TO 5.30PM						

* indicates R/M/T/C/E (Remedial/Makeup/Test/Counseling/Extra)

- 1) EM-II:- Electrical Machine-II
- 2) ELECT-IV :- Elective IV
- 3) HRLP:- Human Rights and Legislative Procedures
- 4) PS-II:- Power System II
- 5) ELECT-V:- Elective V
- 6) MPMC:- Microprocessor and micro Controller

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YEAR: 2019 – 20 SEM: I, WITH EFFECT FROM: 1ST July, 2019

S.Y. ELECTRICAL ENGG. [CLASS ROOM NO. 20]

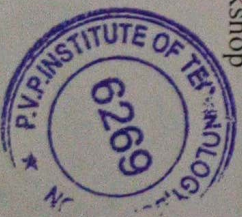
SR.NO.	TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
1	9.30AM TO 10.30AM	NAS[SPP]	SS[SNP]/EEM[VBS]	EE[NSP]	SS[SNP]/EEM[VBS]	S1-M-I[NSP], S2-NAS[SPP], S3-M-I[TSB], S4-EW[MAC]	IITB SPOKEN TUTORIALS
2	10.30AM TO 11.30AM	FMTE[SPM]	FMTE[SPM]	EM-III[UUR]	FMTE[SPM][T]		
11.30 AM TO 12.10PM LUNCH BREAK							
3	12.10PM TO 1.10PM	EM-III[UUR]	EM-III[UUR]	MII[MCB][T]	S1-EW[NSP], S2-M-I[MCB], S3-NAS[SPP], S4-M-I[VBS]	SS[SNP]/EEM[VBS]	OPEN BOOK TEST /TEST
4	1.10PM TO 2.10PM	MII[MCB]	MII[MCB]	NAS[SPP][T]		BHR	EM-III[UUR]* FMTE[SPM]* MII[MCB]*
2.10PM TO 2.20 PM TEA BREAK							
5	2.20PM TO 3.20PM	S1-NAS[SPP], S2-M-I[MCB], S3-EW[NSP], S4-M-I[VBS]	S1-M-I[MAC], S2-EW[NSP], S3-M-I[TSB], S4-NAS[SPP]	LIBRARY HOURS	EM-III[UUR][T]	NAS[SPP]	FMTE[SPM]*/EM-III[UUR]*/EE[NSP]
6	3.20PM TO 4.20PM				BHR	EE[NSP]	SS[SNP]/EEM[VBS]* MII[MCB]* NAS[SPP]*
7	4.30PM TO 5.30PM						

* indicates R/M/T/C/E (Remedial/Makeup/Test/Counseling/Extra)

- 1) SS:- Signals & Systems
- 2) EM-III :- Engg. Mathematics-III
- 3) FMTE :- Fluid Mechanics and Thermal Engg.
- 4) NAS :- Network Analysis & Synthesis
- 5) MI :- Measurement & Instrumentation
- 6) EE :- Engg. Economics
- 7) BHR :- Basic Human Rights
- 8) EW - Electrical Workshop

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T.E. ELECTRICAL

SR NO	TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY	SATURDAY
1	9.30AM TO 10.30AM	DEMCMCB]	DEMCMCB]	DEMCMCB]	SSISYG[TT]	CS-II[SNP]	T2-PS-II[MAC] T1-LIBRARY
2	10.30AM TO 11.30AM	CS-II[SNP]	PS-II[MAC]	CS-II[SNP]	ACM[LSPI]	ACM[LSPI]	
11.30 AM TO 12.10PM LUNCH BREAK							
3	12.10PM TO 1.10PM	SSISYG]	SSISYG]	ACM[LSPI]	T2-LIBRARY T1-ACM[LSPI]	SOFTSKILL	OPEN BOOK TEST /TEST ACM[LSPI/ SSISYG/ DEMCMCB]
4	1.10PM TO 2.10PM	PS-II[MAC]	ACM[LSPI]	SSISYG]		PS-II[MAC]	
2.10PM TO 2.20 PM TEA BREAK							
5	2.20PM TO 3.20PM	T1-PS-II[MAC], T2-STEE[SYG]	T2-DEMCMCB], T1-STEE[SYG]	T1-CS-II[SNP] T2-ACM[LSPI]	PS-II[MAC]	T2-CS-II[SNP], T1-DEMCMCB]	OPEN BOOK TEST /TEST SSISYG*/PS- II[MAC]*/CS- II[SNP]
6	3.20PM TO 4.20PM				CS-II[SNP]		
7	4.30PM TO 5.30PM						

* indicates R/M/T/C/E (Remedial/Makeup/Test/Counseling/Extra)

- 1) DEMC- Digital Electronics and Microcontroller
- 3) CS-II- Control System II
- 5) SS- Signals and Systems

- 2) ACM-AC Machines
- 4) PS-II- Power Systems II
- 6) STEE- Software tools for Electrical Engineers

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